

PRELIMINARY PRODUCT INFORMATION

SHARP



**SHARP's New Line
of MCUs and
System-on-Chips**

APPLICATIONS:	
PDA's	Pocket PCs
Handheld Games	Smart Phones
Media Players	Web Pads
Thin Client	Test and Measurement
GPS	Point of Sale

LH7A404 System-on-Chip

FEATURES

- ARM922T™ Core:
 - 32-bit ARM9TDMI™ RISC Core (200 MHz)
 - 16KB Cache: 8KB Instruction Cache and 8KB Data Cache
 - MMU (WinCE™ Enabled)
- 80KB On-Chip Memory
- Vectored Interrupt Controller
- External Bus Interface
 - 100 MHz
 - Asynchronous SRAM/ROM/Flash
 - Synchronous DRAM/Flash
 - PCMCIA
 - CompactFlash
- Clock and Power Management
 - 32.768 kHz and 14.7456 MHz Oscillators
 - Programmable PLL
- Low Power Modes
 - Run (200 mA), Halt, Standby (35 µA)
- Programmable LCD Controller
 - Up to 1,024 × 768 Resolution
 - Supports STN, Color STN, AD-TFT, HR-TFT, TFT
 - Up to 64 k Colors and 15 Gray Shades
- 10 Channel, 10-bit A/D Converter
 - Touch Screen Controller
 - Brownout Detector
- DMA (12 Channels)
 - External DMA Channels
 - AAC (AC97)
 - MMC
 - USB
- USB Host and Device Interface (USB1.1)
- Synchronous Serial Port (SSP)
 - Motorola SPI™
 - Texas Instruments SSI
 - National Semiconductor MICROWIRE™

- PS/2 Keyboard/Mouse Interface (KMI)
- Three Programmable Timers
- Three UARTs
 - Classic IrDA (115 kbit/s)
- Smart Card Interface (ISO7816)
- DC-to-DC Converters
- MultiMediaCard Interface with Secure Digital (MMC 2.11/SD 1.0)
- AAC (AC97) Codec Interface
- Smart Battery Monitor Interface
- Real Time Clock (RTC)
- Up to 64 General Purpose I/Os
- Watchdog Timer
- JTAG Debug Interface and Boundary Scan
- Operating Voltage
 - 1.8 V Core
 - 3.3 V Input/Output (1.8 V I/O Optional*)
- Temperature
 - -40°C to +85°C Industrial
- Sampling As: 324-Ball PABGA 24 mm × 24 mm
Production As: 324-Ball CABGA 17 mm × 17 mm

DESCRIPTION

The advent of 3G technology opens the door for a wide range of Multimedia applications in mobile information appliances. These appliances require high processing performance and low power consumption. The LH7A404 is designed from the ground up to provide high processing performance, low power consumption, and a high level of integration.

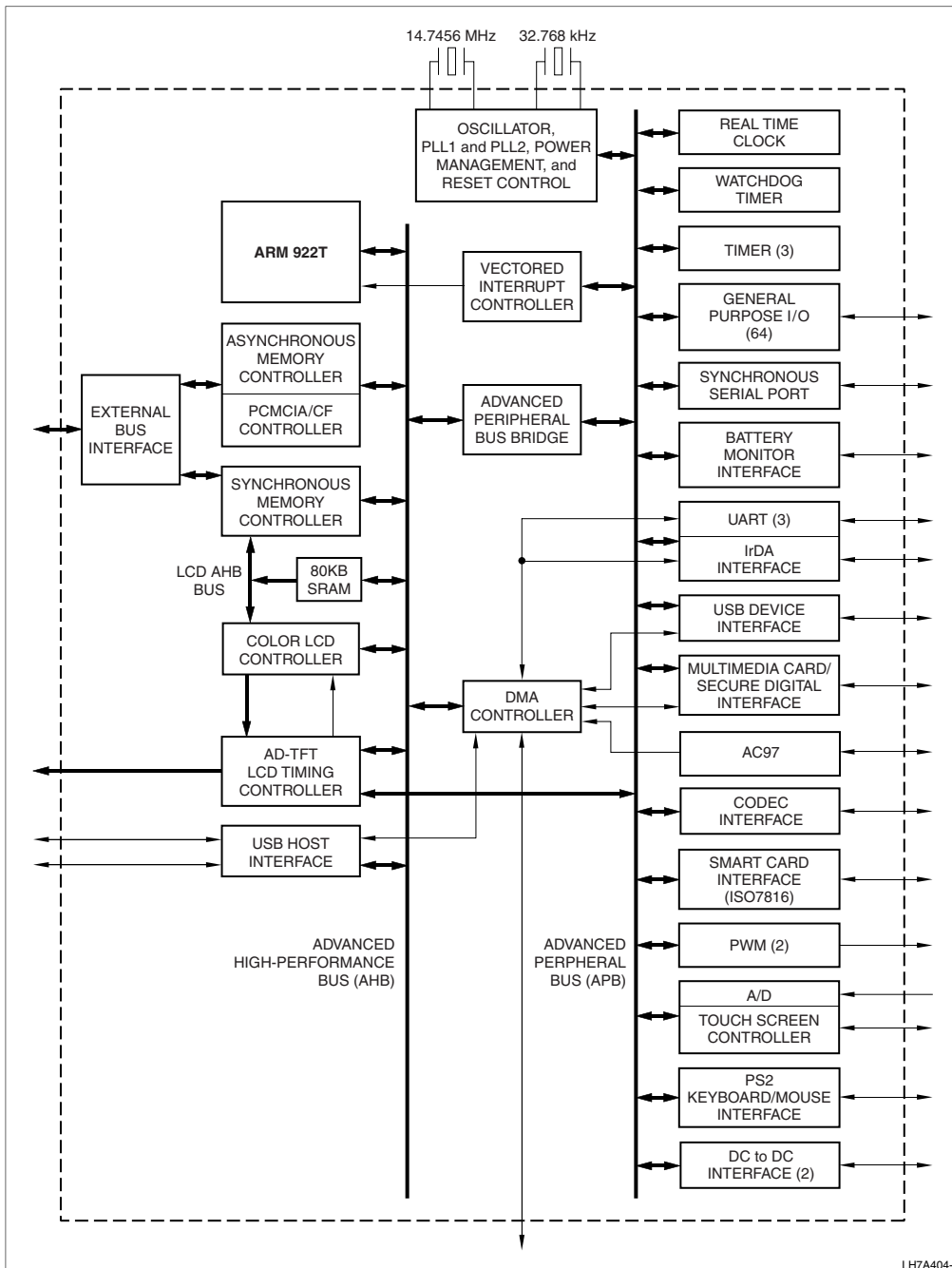
The LH7A404 contains a high performance 32-bit ARM922T Core. Power consumption is reduced by the high level of integration, 80KB on-chip SRAM, fully static design, power management unit, low voltage operation (1.8 V Core, 3.3 V or 1.8 V* I/O) and on-chip PLL.

NOTE: *Under development. Results pending further characterization.

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LH7A404 BLOCK DIAGRAM



SHARP[®]

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