	OF REVISION	ONS	BY	CHKD	DATE	<u> </u>	C	OUNT	DESCRIPTION OF	REVISIONS	BY	CHKD	DAT	IE .	
								Δ							
$\overline{\wedge}$								$\overline{\Delta}$							
APPLICA	BLE STAND	DARD			1	•			t						
RATING	OPERATING TEMPERATURE RANG ING VOLTAGE CURRENT		-30°CTO +85°C(NOTE1) STOR							AGE ERATURE RANGE	-10	°C 1	TO 6	0 °(<u>C</u>
					·										
		SPECIFICATION													
IT	TEST METHOD								REQUIREMENTS					AT	
CONSTRUCTION GENERAL EXAMINATION										ACCORDING TO DRAWING.					×
MARKING	CONFIRMED VISUALLY.												×	×	
ELECTRICAL CHARACTERISTICS															
CONTACT F	100 mA (DC OR 1000 Hz).								30 mΩ MAX.					_	
INSULATION RESISTANCE		500 V DC.								1000 ΜΩ ΜΙΝ.				×	_
VOLTAGE PROOF		650 VAC FOR 1 min.								NO FLASHOVER OR BREAKDOWN.				×	—
MECHAN	VICAL CHA	RACTE	RIST	ics											
MECHANICAL OPERATION		30 TIMES INSERTIONS AND EXTRACTIONS.								 CONTACT RESISTANCE: 30 mΩ MAX. NO DAMAGE, CRACK AND LOOSENESS, OF PARTS. 					-
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.								1 NO ELECTRICAL DISCONTINUITY OF 1 µs. 2 NO DAMAGE, CRACK AND					
										LOOSENESS, OF PARTS. ① NO ELECTRICAL DISCONTINUITY OF X —					
SHOCK	490 m/s ² DURATION OF PULSE 11 ms AT 3 TIMES FOR 3 DIRECTIONS.								① NO ELECTRICAL DISCONTINUITY OF 1 μs. ② NO DAMAGE, CRACK AND					-	
								LOOSENESS,	OF PARTS.			<u> </u>			
	NMENTAL						05.07			O COLUMN OF THE	OIOTANIOE O	20	MAN	1	
DAMP HEAT (STEADY STATE)		EXPOSED AT 40±2°C, 90 TO 95%, 96 h.							 CONTACT RESISTANCE: 30 mΩ MAX. INSULATION RESISTANCE: 500 MΩ MIN. NO DAMAGE, CRACK AND LOOSENESS, OF PARTS. 				×	—	
RAPID CHA	TEMPERATURE -55±3→5 TO 35→85±2→5 TO 35 °C													_	
TEMPERATURE		TIME $30 \rightarrow 5 \rightarrow 30 \rightarrow 5$ min UNDER 5 CYCLES.								② INSULATION RESISTANCE:1000MΩ MIN. ③ NO DAMAGE, CRACK AND LOOSENESS, OF PARTS.					
RESISTANC	SOLDER TEMPERATURE, 250 °C, FOR.								NO DEFORMATION ON CASE OR					-	
SOLDERING HEAT		IMMERSION, DURATION, 5 s.								EXCESSIVE LOOSENESS OF THE TERMINALS. SOLDER SHALL COVER A MINIMUM OF 95					
SOLDERABILITY		SOLDERED AT SOLDER TEMPERATURE, 230 °C FOR IN IMMERSION, DURATION, 3 s.							SOLDER SHALL % OF THE SURF				×		
	NCLUDE TH	E TEMPE	ERAT	URE	RISII	NG BY			awn Me	DESIGNED S. One	CHECKED	APPR	oved purki	RELE/	ASED
CURRENT. Unless otherwise specified, refer to IEC 60512. S. Onc. S. Onc.															
						olicable Te	est				, , , , , ,	· , (U	, ,, 	<u> </u>	
HS.	Note QT: Qualification Test AT: Assurance Test														
HIROSE ELECTRIC CO., LTD. SI LCH TOATION STILLT DF 1 1 E - 2 2 DP - 2 DS A (1 4) CODE NO. (OLD) DRAWING NO. CODE NO. CL 5 4 3 - 0 7 6 0 - 3 - 1 4 7												1/			